L Number	Hits	Search Text	DB	Time stamp
2	1958251	phase detector$1 and (error signal$1 with clock signal$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/04 12:52
3	7274176	first clock circuit and first clock signal$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/04 12:53
4	7013772	second clock circuit and secondl1 and (phase detector$1 and (error signal$1 with clock signal$1)) clock signal$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/04 12:55
5	4619107	loop filter circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/04 12:57
6	6570947	(first clock circuit and first clock signal$1) and (second clock circuit and secondl1 and (phase detector$1 and (error signal$1 with clock signal$1)) clock signal$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/04 13:04
7	2726539	(loop filter circuit) and ((first clock circuit and first clock signal$1) and (second clock circuit and secondl1 and (phase detector$1 and (error signal$1 with clock signal$1)) clock signal$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/04 13:09
8	833171	((loop filter circuit) and ((first clock circuit and first clock signal$1) and (second clock circuit and secondl1 and (phase detector$1 and (error signal$1 with clock signal$1)) clock signal$1))) and (phase detector$1 and (error signal$1 with clock signal$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/04 13:12
9	4737779	phase different near2 first clock signal with second clock signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/04 13:12
10	830929	(((loop filter circuit) and ((first clock circuit and first clock signal$1) and (second clock circuit and secondl1 and (phase detector$1 and (error signal$1 with clock signal$1)) clock signal$1))) and (phase detector$1 and (error signal$1 with clock signal$1))) and (phase different near2 first clock signal with second clock signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/04 13:15
11	631783	filter$3 and (composite error signal$1 with control signal$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/04 13:19
12	310505	((((loop filter circuit) and ((first clock circuit and first clock signal$1) and (second clock circuit and secondl1 and (phase detector$1 and (error signal$1 with clock signal$1)) clock signal$1))) and (phase detector$1 and (error signal$1 with clock signal$1))) and (phase different near2 first clock signal with second clock signal)) and (filter$3 and (composite error signal$1 with control signal$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/04 13:27
13	40	714/699.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/04 13:29